

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS DEVICES	
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Kiran V. Chatty</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: BUR920030188US1</p>		
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<b>Documents being submitted:</b>	<b>Files</b>
us-fee-sheet	BUR920030188US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920030188US1-usidst.xml us-ids.dtd us-ids.xsl
us-request	BUR920030188US1-usrequ.xml us-request.dtd us-request.xsl
us-assignment	BUR920030188US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030188Assign1.tif BUR920030188Assign2.tif
us-declaration	BUR920030188dec1.tif
us-declaration	BUR920030188dec2.tif
application-body	BUR920030188-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd BUR920030188drw1.tif BUR920030188drw2.tif BUR920030188drw3.tif BUR920030188drw4.tif BUR920030188drw5.tif

**Comments**

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